

ISSUE CLASSIFICATION	
Class	Subclass

**U.S. UTILITY PATENT APPLICATION**

**PATENT DATE**

Q.I.P.E.  
SCANNED 7951 Q.A.

Shinichi Imai

Semiconductor device and method for fabricating the same

PTO-2040  
12/99

**PREPARED AND APPROVED FOR ISSUE**

**ISSUING CLASSIFICATION**

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>	<b>Print Claim for O.G.</b>
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.  _____				<b>NOTICE OF ALLOWANCE MAILED</b>	
	(Assistant Examiner) _____ (Date)				
<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____.  _____  _____				<b>ISSUE FEE</b>	
	(Primary Examiner) _____ (Date)			<b>Amount Due</b>	<b>Date Paid</b>
<input type="checkbox"/> c) The terminal ____ months of this patent have been disclaimed.				<b>ISSUE BATCH NUMBER</b>	
	(Legal Instruments Examiner) _____ (Date)				
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(LABEL AREA)